ABOUT DEPARTMENT OF FCE

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Shri Ram Murti Smarak Institutions Bareilly • Lucknow

- Shri Ram Murti Smarak College of Engineering & Technology, Bareilly
- Shri Ram Murti Smarak Institute of Medical Sciences, Bareilly
- SRMS College of Engineering, Technology & Research, Bareilly
- Shri Ram Murti Smarak Nursing School, Bareilly
- · R.R. Cancer Hospital, Bareilly
- Shri Ram Murti Smarak International Business School, Lucknow
- Shri Ram Murti Smarak College of Engineering and Technology, Unnao
- Shri Ram Murti Smarak Institute of Paramedical Sciences, Bareilly
- Shri Ram Murti Smarak Functional Imaging & Medical Centre, Lucknow
- · Shri Ram Murti Smarak Hospital, Unnao
- · Shri Ram Murti Smarak City Hospital, Bareilly
- Round Table India Ladies Circle India-SRMS Charitable School, Unnao

FACULTY DEVELOPMENT PROGRAMME

Microsystems & Advanced Semiconductor Devices

July 3 -7, 2018



Organized by:

Shri Ram Murti Smarak
College of Engineering & Technology, Bareilly

Ram Murti Puram, 13 KM. Bareilly - Nainital Highway, Bareilly - 243 202 (U.P.)

Phone: (0581) 2582246-49, 2582331-32 Fax: (0581) 2582330,

Website: www.srms.ac.in. E-mail: infocetbly@srmscet.edu

Sponsored by

TEQIP III & Dr. A.P.J. Abdul Kalam Technical University, Lucknow, Uttar Pradesh

Motivator & Guide

Prof. (Dr.) Vinay Kumar Pathak Hombie V.C., Dr. APJAKTU, Lucknow

Resource Persons

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Dr. Aloke Dutta

Or. Sudeb Das Gupta IIT Roorkee

Dr. Santesh Kr. Gupta

Mr. Ankur Sengal CorEL Technologies, New Delhi

Chief Patron Shri Dev Murti

Patron Shri Aditya Murti

Secretary, SRMS Trust

Advisors

Er. Subhash Mehra rust Administrator, SRMS Trust

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Ms. Ankita Gupta Mob. 0458702127 Email: - ankita papta@amaceta

Mrs. Namrata Gupta Mon : 0458702516

AIM & SCOPE OF THE FDP:

This FDP will provide an unparalleled knowledge gathering initiative for diversified topics of discussion and insights in unraveling the wonders in the field of "Microsystems & Advanced Semiconductor Devices" which also paves way for generations of researchers and scholars in acquiring knowledge and information by meeting face to face with the experts. Discussion on the research results from micro scale semiconductor devices to nanoscale semiconductor devices and their modelling will reduce the gap between the academic and industry approach in setting the new standard.

OVERVIEW:

Device scaling for optimum performance, least power consumption & miniaturization requires the introduction of advanced process module, smart material technology, innovative device architecture and use of alternative devices compared to standard technology. CMOS technology is at its final phase of its life cycle, new materials and device concept needs attention and explorations to optimize the potential even more than Moore's Approach. Going beyond 'Si' with group IV alloys can provide higher efficiency and enable new device concepts in a compatible material system.

This FDP will provided an extensive coverage on the following topics:

- Semiconductor single crystal growth
- Nano-materials and devices.
- Polysilicon for Photo-Voltaic applications
- Patterning challenges for Monolithic Si Photonics
- Semiconductor Physics & Modeling
- Microelectronics Technology
- Wide Bandgap Semiconductor Devices and Circuit Model
- Beyond CMOS Technology
- Semiconductor in Microwave Signal Generation and Processing
- Lab Sessions on Xilinx Vivado

PEDAGOGY:

The sessions of this Faculty Development Program shall be conducted by the team of eminent academicians & industry experts possessing in-depth knowledge in the area of Micro Systems & Advanced Semiconductor Devices. It will consist of a series of lectures and hands-on practical sessions.

ELIGIBLE PARTICIPANTS:

All the faculty members of engineering colleges affiliated to Dr. A.P.J.A.K. Technical University, Lucknow can join this FDP to enhance their skills and expertise in the area.

CERTIFICATION:

The certificate of participation will be provided to all the participants at the end of the program.

ACCOMODATION:

The stay arrangement of outstation participants can be done in campus on nominal charges. The request for accommodation may be made in registration form.

REGISTRATION DETAILS:

- There is NO FEE for Registration. The registration will be done on first come first serve basis.
- The participants are requested to register their name by mailing a scanned copy of registration form to the email; smistdp.ec.2018@gmail.com
- The registration form can be downloaded from http://www.srms.ac.in/cet.
- The confirmation of registration will be done by email.
- Last Date of Registration: 25th June, 2018.